Lesson 3.4 – Programmable Logic Devices

**Concepts**

1.      A PLD is a digital IC capable of being programmed to provide specific logic functions.

2.      PLD family consists of a variety of device architectures and configurations.

3.      Most common PLDs are based on AND/OR logic gate arrays and are programmable with SOP expressions.

4.      PLDs can be programmed, erased and reprogrammed many times allowing easier design modifications.

5.      PLDs offer design flexibility, reduce board space and package count and can be used to develop digital designs more quickly than fixed –function logic.

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| **Performance Objectives** | **Standards** |
| Students will be able to design and implement combinational logic circuits using reprogrammable logic devices. | Science 2.6Science 4.1Math 2.2Math 2.3Math 5.1Math 6.1Math 6.2Math 6.3Math 6.4Math 8.4Math 9.2Math 9.3Math 10.1Math 10.2Math 10.3Technology 1.3Technology 3.1Technology 3.3Technology 4.1  |
| Students will create PLD logic files that define combinational circuit designs using Boolean Expressions. | Science 1.2Science 4.2Math 2.2Math 2.3Math 5.1Math 6.1Math 6.2Math 6.3Math 6.4Math 8.4Math 9.2Math 9.3Math 10.1Math 10.2Math 10.3Technology 1.3Technology 3.1Technology 3.3Technology 4.1  |
| Students will understand and use logic compiler software to create JEDEC files for programming PLDs. | Science 4.2Science 5.6Math 2.2Math 2.3Math 5.1Math 6.1Math 6.2Math 6.3Math 6.4Math 8.4Math 9.2Math 9.3Math 10.1Math 10.2Math 10.3Technology 1.3Technology 3.1Technology 3.3Technology 4.1 |

**Anticipatory Set**

Teacher Demonstration:

Show the students a fully assembled, fully functional solution to the problem you developed for the Unit Anticipatory set using PLDs.

**Key Terms**

|  |  |  |
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| Compile | JEDEC  | PLD |
| GAL | PAL |   |

**Key Questions**

1.      What is a PLD and what are the advantages of using them?

**Activities**

Day 1:

         Teacher will present to the class, in either oral or written form, the Lesson Key Question.

         Teacher will present a lesson on PLDs.

         Students will start activity 3.4.1..

Day 2:

         Teacher will review Activity 3.4.1 with students.

         Students will begin Activity 3.4.2.

Day 3:

         Students will complete Activity 3.4.2, including breadboarding and testing the circuit.

Days 4 :

      Teacher will review Activity 3.4.2 with students to ensure understanding.

       Teacher will assignactivity 3.4.3 (Note to the teacher: The problems in Activity 5.3C are listed in order of difficulty – from easy to hard. It is not necessary that students do ALL the problems in the Activity, though they should do a range of levels of difficulty. Which problems students do is left for you to decide.)

Day 5-6:

* Students will work on Activity 3.4.4

**Resources**

PowerPoints

Programmable Logic Devices

Word Documents

Activity 3.4.1 – Introduction to PLDs

Activity 3.4.2 – A Basic Problem

Activity 3.4.3 – Additional Problems

Activity 3.4.4 – PLD DesignProblems